

FIG. 1 (PRIOR ART)

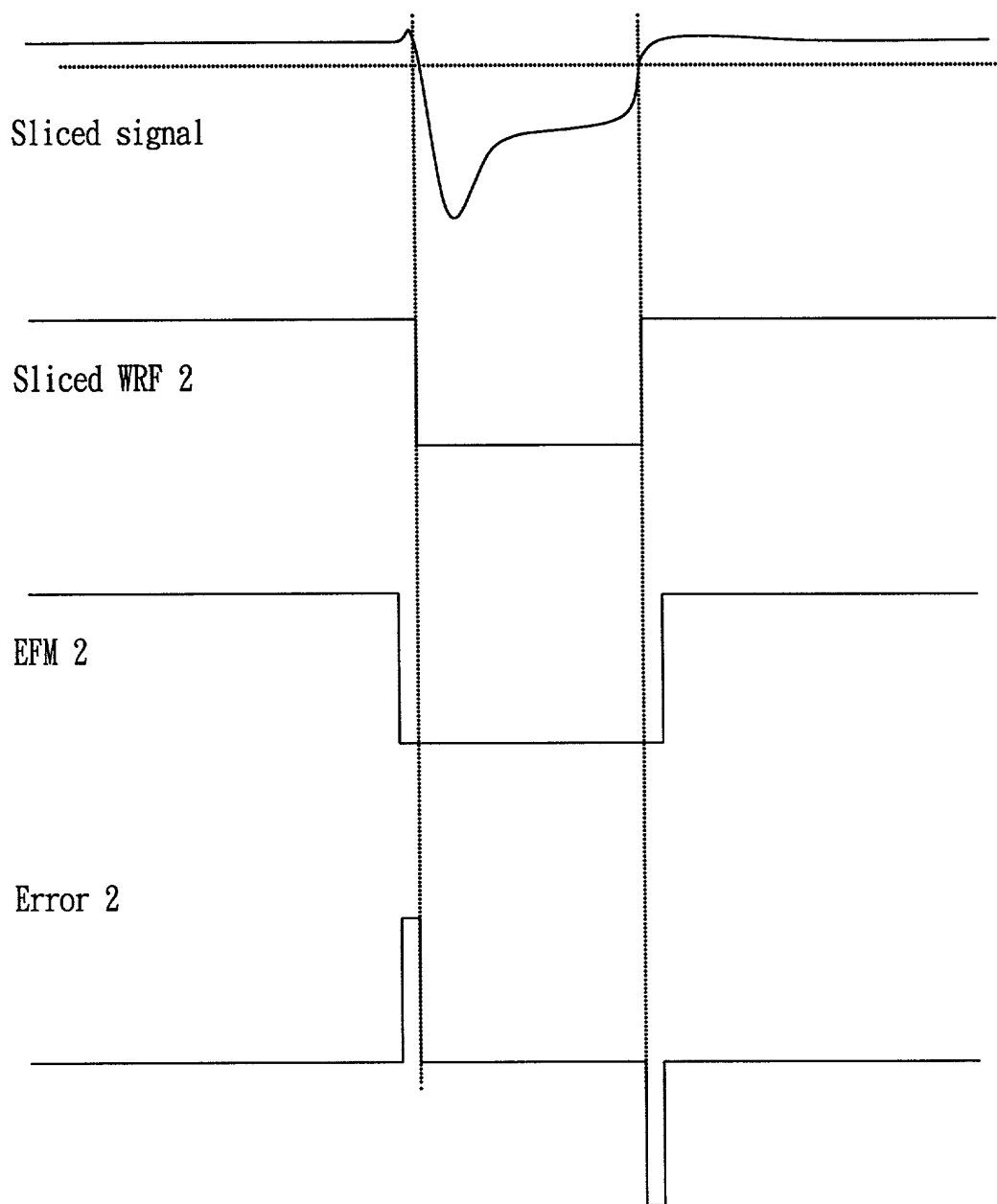


FIG. 2 (PRIOR ART)

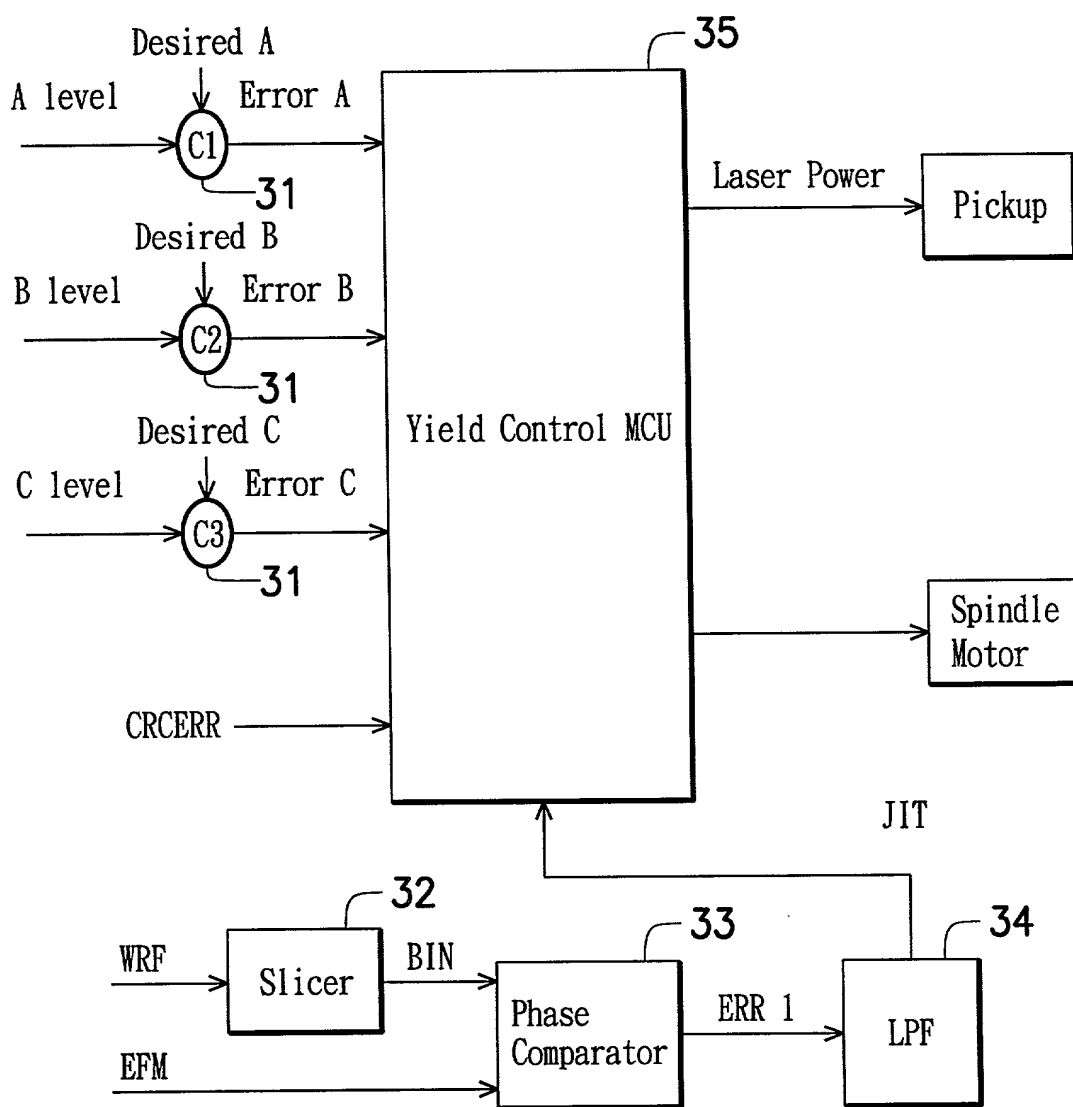


FIG. 3

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graph TD
    S41[S41: determining the number of wrong detection code to be input] --> S42[S42: decreasing the recordable rate if the number of wrong detection code to be input is over a first predetermined number]
    S42 --> S43[S43: respectively comparing the first, second, third input signal levels with the first, second, third predetermined signal levels]
    S43 --> S44[S44: adjusting a write power based on the comparison results]
    S44 --> S45[S45: adjusting a write delay based on a jitter input value]
    S45 --> S46[S46: adjusting the current recordable rate and a write delay table based on the adjusted write power and write delay to control the rotation rate of spindle motor and the write action of the pickup in the CD recorder]

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FIG. 4

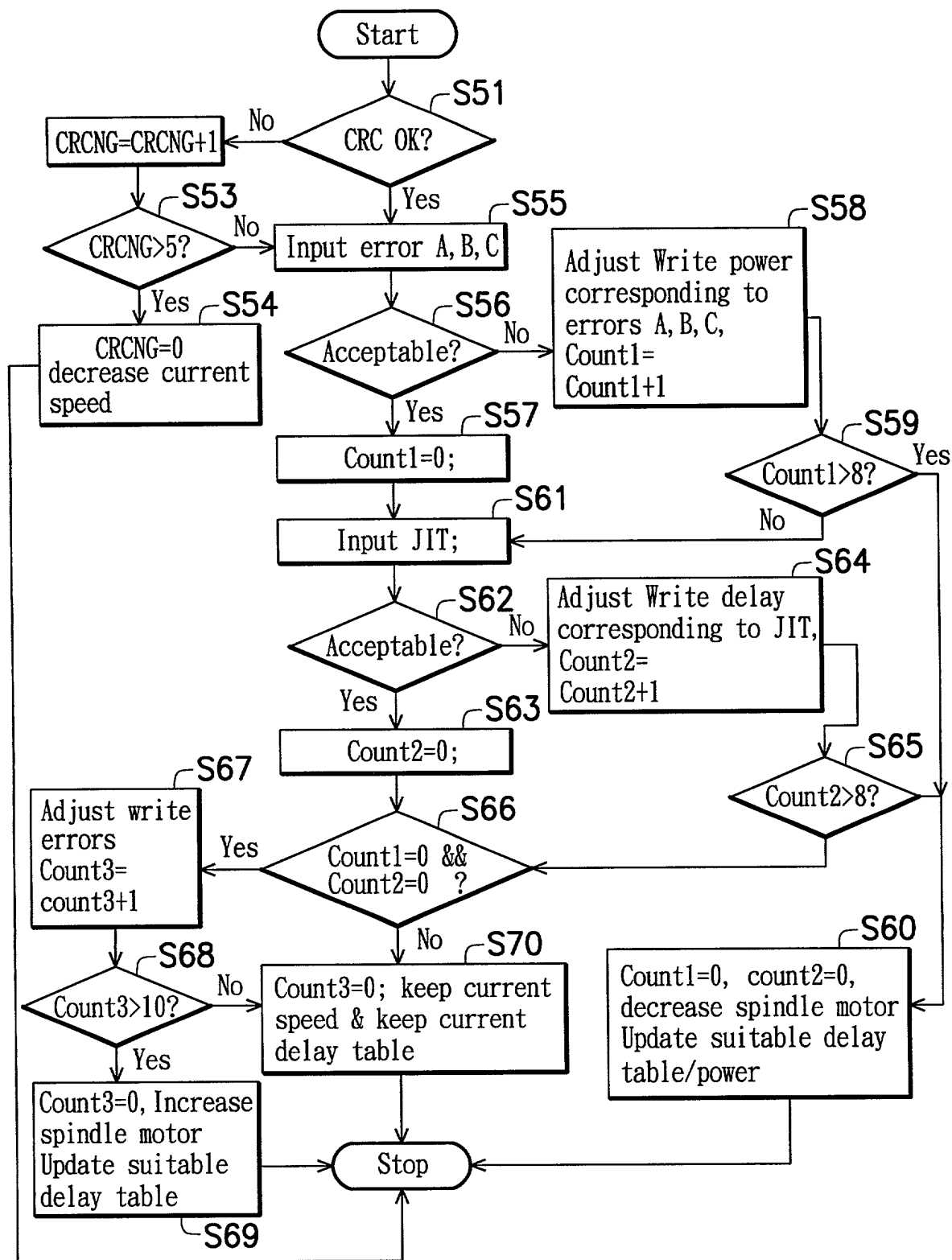


FIG. 5